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#5/Pre-Amst-19 1-16-02 PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

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Application No.: New U.S. Patent Application

Filed: May 30, 2001

Docket No.: 109677

For: MEMORY DEVICE

PRELIMINARY AMENDMENT

Director of the U.S. Patent and Trademark Office Washington, D. C. 20231

Sir:

Prior to initial examination, please amend the above-identified application as follows:

IN THE CLAIMS:

Please replace claims 3, 5, 10 as follows:

3. (Amended) A memory device as claimed in claim 1, wherein a common electrode is provided between two layers, an input electrode is provided on one of the layers and an output electrode is provided on the other of the layers, the input and output electrodes being disposed on opposite sides of their respective layers compared with the common electrode.

5. (Amended) A memory device comprising a plurality of devices as claimed in

claim 1.

10. (Amended) A method as claimed in claim 7, wherein the step of detecting the said resultant voltage comprising the phase of the said resultant voltage with the phase of the said applied non-polarising voltage.